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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Vijay K. Reddy, et al.

Serial No.: 10/814,453

Filed: 03/31/04

For: Voltage Waveform Generation Circuit

TI-37048

Examiner: Jeff W. Natalini

Art Unit: 2858

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF APPEALS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION I hereby certify that the above correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office at 571-273-8300 on the date shown below:	
<u>Karen Vertz</u> Karen Vertz	<u>3-27-06</u> Date

Applicant hereby appeals claims 1-5 and 7-10 to the Board of Appeals from the decision dated December 6, 2005 of the Primary Examiner finally rejecting claims 1-5 and 7-10.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection  
☐ was granted on \_\_\_\_\_.  
☐ is requested for \_\_\_\_\_ month(s).
2. ☒ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. ☒ Fee \$500.00:  
☐ Not required (Fee paid in prior appeal)  
☒ The Commissioner of Patents is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 20-0668 of Texas Instruments Incorporated.

Signature [Rule 191(b)]

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